Load Upper Immediate (Top 20 bits of rd)

Load Half

Load Byte

Store Byte

Store Half

Store Word

Add Immediate

Set Less Than Immediate

Xor Immediate

Or Immediate

And Immediate

Shift Left Logical Immediate

Shift Right Logical Immediate

Shift Right Arithmetic Immediate

Add

Subtract

Set Less Than Unaligned

Xor

Or

And

Fence

Fence Instruction

Load Word Unaligned

Load Double

Store Double

Shift Left Logical Immediate

Shift Right Logical Immediate

Shift Right Arithmetic Immediate

Add Immediate Word

Shift Left Immediate Word

Shift Right Immediate Word

Shift Right Arithmetic Immediate Word

Add Word

Subtract Word

Shift Left Word

Shift Right Word

Shift Right Arithmetic Word

Multiply

Multiply High Signed Signed

Multiply High Signed Unsigned

Divide Signed

Divide Unsigned

Remainder Signed

Remainder Unsigned

Multiply Word

Divide Signed Word

Divide Unsigned Word

Remainder Signed Word

Remainder Unsigned Word

16-bit comma separated words (unsigned)

32-bit comma separated words (unsigned)

64-bit comma separated words (unsigned)

16-bit comma separated words (signed)

32-bit comma separated words (naturally aligned)

8-bit comma separated words

8-bit thread local word

signed little endian base 128, DWARF

unsigned little endian base 128, DWARF

emit string (alias for string)

emit string

emit the included files as a binary sequence of octets

emit integer

align to power of 2 (alias for p2align)

byte align

align to power of 2

emit symbol name to symbol table (scope GLOBAL)

emit symbol name to symbol table (scope LOCAL)

constant definition

emit text section (if not present) and make current

emit data section (if not present) and make current

emit rodata section (if not present) and make current
emit bss section (if not present) and make current
emit common object to bss section
emit common object to bss section
emit section
emit section
begin macro definition
begin macro definition
begin macro definition
accepted for source compatibility
accepted for source compatibility
accepted for source compatibility
accepted for source compatibility
No operation
addi zero zero
addi rd rs 0
xor rd rs -1
sub rd x0 rs
subw rd x0 rs
addiw rd rs 0
sltiu rd rs 1
slti rd x0 rs
slti rd x0 rs
fsgnj.s frd frs frs
fsgnj.s frd frs frs
fsgnj.s frd frs frs
fsgnj.d frd frs frs
fsgnj.d frd frs frs
beq x0 rs x0
breq rs x0 offset
beq x0 rs x0
beq x0 rs x0
biff x0 rs offset
biff x0 rs offset
biff x0 rs offset
biff x0 rs offset
biff x0 rs offset
biff x0 rs offset
biff x0 rs offset
biff x0 rs offset
biff x0 rs offset
jal x0 offset
jal x0 offset
jal x0 offset
Return from subroutine
jal x0 x1 0

Syntax high 12[10:5] = Bits 12 & 10-5 of immediate (other bits in other part)