			F E D C B A 9 8 7 6 5 4 3 2 1 0	F E D C B A 9 8 7 6 5 4 3 2 1 0		
	LA dest,addr	Load address				
I	**LB** dest,addr	Load byte				
I	**LBU** dest,addr	Load byte unsigned				
	LH dest,addr	Load halfword				
I	**LHU** dest,addr	Load halfword unsigned				
I	**LW** dest,addr	Load word				
	LWL dest,addr	Load word left				
	LWR dest,addr	Load word right				
	LD dest,addr	Load double				
	ULH dest,addr	Unaligned Load Halfword				
	ULHU dest,addr	Unaligned Load Halfword Unsigned				
	ULW dest,addr	Unaligned Load Word				
	LI dest,expr	Load Immediate				
I	**LUI** dest,expr	Load Upper Immediate				
I	**SB** source,addr	Store Byte				
	SD dest,expr	Store Doubleword				
I	**SH** dest,expr	Store Halfword				
	SWL dest,expr	Store Word Left	1 0 1 0 1 0 s s	s s s t t t t t	i i i i i i i i	i i i i i i i i
	SWR dest,expr	Store Word Right	1 0 1 1 1 0 s s	s s s t t t t t	i i i i i i i i	i i i i i i i i
I	**SW** dest,expr	Store Word	1 0 1 0 1 1 s s	s s s t t t t t	i i i i i i i i	i i i i i i i i
	USH dest,expr	Unaligned Store Halfword				
	USW dest,expr	Unaligned Store Word				
R	**ADD** dest,src1,src2	Add (With Overflow)				
I	**ADDI** dest,src1,imm	Add Immediate				
I	**ADDIU** dest,src1,imm	Add Immediate Unsigned				
R	**ADDU** dest,src1,src2	Add (Without Overflow)				
R	**AND** dest,src1,src2	AND				
I	**ANDI** dest,src1,imm	And Immediate				
R	**DIV** dest,src1,src2	Divide (Signed)				
R	**DIVU** dest,src1,src2	Divide (Unsigned)				
R	**XOR** dest,src1,src2	Exclusive OR	0 0 0 0 0 0 s s	s s s t t t t t	d d d d d 0 0 0	0 0 0 1 0 1 1 0
	XORI dest,src,imm	Xor Immediate	0 0 1 1 1 0 s s	s s s t t t t t	i i i i i i i i	i i i i i i i i
	MUL dest,src1,src2	Multiply				
	MULO dest,src1,src2	Multiply with Overflow				
	MULOU dest,src1,src2	Multiply with Overflow Unsigned				
R	**NOR** dest,src1,src2	Not Or				
R	**OR** dest,src1,src2	OR				
I	**ORI** dest,src1,imm	OR Immediate				
	SEQ dest,src1,src2	Set Equal				
	SGT dest,src1,src2	Set Greater				
	SGE dest,src1,src2	Set Greater/Equal				
	SGEU dest,src1,src2	Set Greater/Equal Unsigned				
	SGTU dest,src1,src2	Set Greater Unsigned				
R	**SLT** dest,src1,src2	Set Less				
I	**SLTI** dest,src,imm	Set on Less Than Immediate				
I	**SLTIU** dest,src,imm	Set on Less Than Unsigned				
	SLE dest,src1,src2	Set Less/Equal				
	SLEU dest,src1,src2	Set Less/Equal Unsigned	0 0 0 0 0 0 s s	s s s t t t t t	d d d d d 0 0 0	0 0 1 0 1 0 1 0
R	**SLTU** dest,src1,src2	Set Less Unsigned				
	SNE dest,src1,src2	Set Not Equal				
R	**SUB** dest,src1,src2	Subtract (With Overflow)	0 0 0 0 0 0 s s	s s s t t t t t	d d d d d 0 0 0	0 0 1 0 0 0 1 0
R	**SUBU** dest,src1,src2	Subtract (Without Overflow)	0 0 0 0 0 0 s s	s s s t t t t t	d d d d d 0 0 0	0 0 1 0 0 0 1 1
	REM dest,src1,src2	Remainder (Signed)				
	REMU dest,src1,src2	Remainder (Unsigned)				
	ROL dest,src1,src2	Rotate Left				
	ROR dest,src1,src2	Rotate Right				
R	**SRA** dest,src1,src2	Shift Right Arithmatic	0 0 0 0 0 0 0 0	0 0 0 t t t t t	d d d d d i i i	i i 0 0 0 0 1 1
R	**SLL** dest,src1,src2	Shift Left Logical				
R	**SRL** dest,src1,src2	Shift Right Logical	0 0 0 0 0 0 0 0	0 0 0 t t t t t	d d d d d i i i	i i 0 0 0 0 1 0
	ABS dest,src	Absolute value				
	NEG dest,src	Negate (With overflow)				
	NEGU dest,src	Negate (Without overflow)				
	NOT dest,src	Not Or				
	MOVE dest,src	Move				
R	**MULT** src1,src2	Multiply				
R	**MULTU** src1,src2	Multiply (Unsigned)				
J	**J** addr	Jump				
J	**JAL** return,target	Jump and link				
R	**JR** reg	Jump to address in register				
I	**BEQ** src1, src2, label	Branch on Equal				
	BGT src1, src2, label	Branch on Greater				
	BGE src1, src2, label	Branch on Greater/Equal				
	BGEU src1, src2, label	Branch on Greater/Equal unsigned				
	BGTU src1, src2, label	Branch on Greater unsigned				
	BLT src1, src2, label	Branch on Less than				
	BLE src1, src2, label	Branch on Less/Equal				
	BLEU src1, src2, label	Branch on Less/Equal Unsigned				
	BLTU src1, src2, label	Branch on Less Unsigned				
I	**BNE** src1, src2, label	Branch on Not Equal				
	BEQZ src1, label	Branch on Equal to Zero				
	BGEZ src1, label	Branch on greater/equal to zero				
I	**BGTZ** src1, label	Branch on Greater than zero				
	BGEZAL src1, label	Branch on Greater or equal to zero				
	BLTZAL src1, label	Branch on less than zero and link				
I	**BLEZ** src1, label	Branch on Less than or equal to zero				
	BLTZ src1, label	Branch on less than zero and link				
	BNEZ src1, label	Branch on not equal to zero				
	B label	Branch				
	BAL label	Branch and Link				
	BREAK breakcode	Break				
	RFE	Restore from exception				
	SYSCALL	System Call	0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0	0 0 0 0 1 1 0 0	
R	**MFHI** register	Move from HI Register				
R	**MTHI** register	Move to HI Register				
R	**MFLO** register	Move from Low register				
R	**MTLO** register	Move to Low Register				
	LWCz dest,addr	Load Word Coprocessor Z				
	SWCz source,addr	Store Word Coprocessor z				
R	**MFCz** dest-gpr,source	Move from Coprocessor z				
	MTCz src-gpr, destination	Move to Coprocessor z				
	BCzF label	Branch Coprocessor z false				
	BCzT label	Branch Coprocessor z true				
	Cz expr	Coprocessor z operation				
	CFCz dest-gpr, source	control from Coprocessor z				
	CTCz src-gpr,destionation	control to Coprocessor z				
	NOP	No Operation				